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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. ....09/609,019  
 Filing Date ..... Jun 30, 2000  
 Inventorship ..... Yacobi et al.  
 Applicant..... Microsoft Corporation  
 Group Art Unit .....2171  
 Examiner ..... HOFFMAN, BRANDON S  
 Attorney's Docket No. .... MS1-847US  
 Title: *Methods and Apparatus for Protecting Information Content*

**PETITION TO WITHDRAW FROM ISSUE  
 UNDER 37 CFR §1.313 AND REQUEST FOR  
 CONTINUED EXAMINATION UNDER 37 CFR  
 §1.114**

To: Commissioner for Patents  
 P.O. Box 1450  
 Alexandria VA 22313-1450

From: Kasey C. Christie (Tel. 509-324-9256; Fax 509-323-8979)  
 Customer No. 22801

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Serial No.: 09/609,019  
 Any Docket No.: MS1-847US  
 Petition to Withdraw from Issue under 37 CFR §1.313  
 and Request for Continued Examination under 37 CFR

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**CLAIM AMENDMENTS**

None.

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## SPECIFICATION AMENDMENTS

On page 15, please replace the paragraphs from line 1-25 with the following paragraphs:

~~Fig. 9 is omitted and thus not described herein.~~

Fig. ~~10~~ 9 illustrates a display device capable of decrypting and displaying video signals generated by the display adapter of Fig. 8.

~~Fig. 11 is omitted and thus not described herein.~~

Fig. ~~12~~ 10 illustrates a value mapping circuit of the present invention.

~~Fig. 12 is omitted and thus not described herein.~~

~~Fig. 14 is omitted and thus not described herein.~~

Fig. ~~15~~ 11 illustrates a MUX suitable for use in the value mapping circuit illustrated in Fig. ~~12~~ 10.

On page 42, please replace the paragraph starting at line 11 with the following:

The steps 700, are performed by the video signal encryption circuit 806, to encrypt R, G, B video signals as a function of the mapped set A of permutation matrix values, ~~are shown in Fig. 9.~~

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1 On page 44, please replace the paragraphs from line 8 to line 31 with the  
2 following:  
3

4 Having described encryption of the analog R, G, B video  
5 signals as performed by the display adapter 848, decoding of the  
6 generated composite encrypted R', G' B' signals will now be  
7 described with reference to Fig. 9, ~~Fig. 10 and 11~~.

8 Fig. 9 ~~Fig. 10~~ illustrates a display device 947 which is capable  
9 of receiving and decrypting the R', G' B' encrypted analog video  
10 signals generated by display adapter 848. The display device 947  
11 includes many components which are the same as, or similar to,  
12 those previously discussed with regard to Fig. 6. Such components  
13 are identified in Fig. 9 ~~Fig. 10~~ using the same reference numerals as  
14 used in Fig. 6 and will not be described again in detail. Note that the  
15 display device 947 includes a pseudo random number generator 810  
16 and video signal decryption circuit 906 ~~encryption circuit 806~~ which  
17 perform similar functions to those of the like named Fig. 6  
18 components.  
19

20 Due to implementation issues relating to the decryption  
21 circuit 906, and the differences in the implemented analog signal  
22 encryption between the Fig. 6 and Fig. 9 ~~Fig. 10~~ embodiments, these  
23 circuits may be implemented using hardware and/or software that  
24 ~~differs~~ differ from that used to implement the like named circuits  
25 found in Fig. 6.

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1 On page 47, please replace the paragraph starting at line 1 with the following:

2  
3 ~~One~~ In one particular embodiment of the signal decryption  
4 process 750 performed by video signal decryption circuit 906 ~~is~~  
5 ~~illustrated in Fig. 11. In Fig. 11,~~ the value 4 is not swapped for the  
6 value 3, e.g., A3 is not set to A4, and k=3 is used to designate the R  
7 signal line. ~~As illustrated the~~ In this particular embodiment, the  
8 process begins in start step 752 wherein the set A of mapped matrix  
9 values including vectors A1, A2, A3 is received by the decryption  
10 circuit 906. The value  $\alpha$  may also be received in start step 752.

11  
12 On page 49, please replace the paragraph starting at line 13 with the following:

13  
14 The decryption process 750 performed by the video  
15 decryption circuit 906 ~~is illustrated in Fig. 11~~ ~~As illustrated the~~  
16 ~~decryption process~~ begins in start step 752 wherein the encrypted  
17 analog video signals R', G' and B' are received as well as the set A of  
18 values of the mapped permutation matrix used to encrypt the signals  
19 which are to be decrypted.  $\alpha$  is also received in step 752 in  
20 embodiments where  $\alpha$  is not fixed, e.g., set to 1. Operation proceeds  
21 from start step 752 to step 754 wherein one of a first through third  
22 decrypted video signal  $V_{(R',G')}$ ,  $V_{(G',B')}$ ,  $V_{(B',R')}$ , is generated for each  
23 pair (R',G'), (G',B'), (B', R') of encrypted video signals.  
24  
25

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1 On page 52, please replace the paragraphs from line 9-25 with the following:

2  
3 An exemplary value mapping circuit 808, which may be used  
4 in the display adapter 848 and display device 947, is illustrated in  
5 ~~Fig. 12~~ Fig. 10. The illustrated embodiment assumes that the 9  
6 values in the set A' of permutation matrix values are supplied to the  
7 value mapping circuit 808 in parallel, e.g., each on a different line.

8 The value mapping circuit 808 comprises first through third  
9 sets of multiplexers 1002, 1004, 1006 coupled together as shown in  
10 ~~Fig. 12~~ Fig. 10. The first set of multiplexers 1002 is responsible for  
11 processing the three values comprising A1, corresponding to the first  
12 row of the matrix A. Similarly, the second and third sets of  
13 multiplexers 1004, 1006 are responsible for processing the values  
14 A2, A3 in the second and third rows of the matrix A, respectively.

15  
16 On page 53, please replace the paragraph starting on line 18 with the following:

17  
18 The permutation matrix coefficient supplied to the control  
19 input of each MUX 1010, 1012, 1014 is used to determine whether  
20 the signal supplied to the first or second data inputs of each MUX  
21 will be output. In the ~~Fig. 12~~ Fig. 10 embodiment, a value of 0  
22 supplied to a MUX control input will result in the signal  $+\alpha$ , e.g., 1  
23 being output. However, a value of 1 supplied to a MUX control  
24 input will result in the signal  $-\alpha$ , e.g., -1 being output. Thus,  
25 MUXes 1010, 1012, 1014 provide a relatively simple way to map

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1 the set A' of 0 and 1 values to the  $+\alpha$  and  $-\alpha$  values of the mapped  
2 permutation matrix A.

3  
4 On page 54, please replace the paragraphs from line 1-19 with the following:

5  
6 ~~Fig. 15~~ Fig. 11 illustrates a MUX ~~1500~~ 1100 suitable for use  
7 as any one of the MUXes 1010, 1012, 1014. The MUX ~~1500~~ 1100  
8 comprises first and second analog pass gates ~~1502, 1504~~ 1102, 1104.  
9 The first analog pass gate ~~1502~~ 1102 has a negated control input  
10 while the second analog pass gate ~~1504~~ 1104 has a standard control  
11 input. The value  $+\alpha$ , e.g., +1, is supplied to the signal input of the  
12 first pass gate ~~1502~~ 1102 while the value  $-\alpha$ , e.g., -1, is supplied to  
13 the signal input of the second pass gate ~~1502~~ 1102. The outputs of  
14 the first and second pass gates are both coupled to the output of the  
15 MUX ~~1500~~ 1100.

16 As a result of the negated input on the first pass gate ~~1502~~  
17 1102, the input signal (+1) to the first pass gate ~~1502~~ 1102 will be  
18 output by the MUX ~~1500~~ 1100 when the control signal input is a 0.  
19 Since the control input of the second pass gate ~~1504~~ 1104 is not  
20 negated, the input signal (-1) to the second pass gate ~~1504~~ 1104 will  
21 be output by the MUX ~~1500~~ 1100 when the control signal is 1.

## REMARKS

This is both a petition to withdraw from issue under 37 CFR §1.313 and request for continued examination under 37 CFR §1.114. The claims in this case have been allowed and the issue fee has been paid.

### Petition To Withdraw From Issue Under 37 CFR §1.313

In April of 2005, Examiner Hoffman discussed with the undersigned Attorney for the Applicant, Kasey Christie, an issue raised during the issuance process. There is apparently a need to further amend the specification and the drawings so that all referenced drawings are cited in numerical sequence without skipping any numbers in the sequence.

However, there appears to be no direct mechanism allowing for the correction of the specification after the issue fee has been paid. Therefore, in accordance with the instructions of Examiner Hoffman, Applicant formally petitions the Office to withdraw the above-identified patent application (Serial No.: 09/609,019) from issue under 37 CFR §1.313.

### Request For Continued Examination Under 37 CFR §1.114

Accordingly, Applicant amends the specification herein and submits herewith updated drawings. More specifically, Applicant files for a request for continued examination (RCE).

Along with such a request, Applicant submits amendments to the specification and the drawings which are intended to correct the issues which were

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1 raised during the issuance process. Applicant submits that the application as now  
2 amended is fully allowable in both substance and form.  
3

4 Respectfully Submitted,

5  
6 Dated: 5-24-05

By: 

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